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SERIAL NUMBER 09/703,181	FILING DATE 10/30/2000 RULE -	CLASS 375 716	GROUP ART UNIT 2631 2825	ATTORNEY DOCKET NO. CY-0016
APPLICANTS Michael T. Moore, Santa Clara, CA ; James Lie, Milpitas, CA ;				
** CONTINUING DATA ***** <i>None AC</i>				
** FOREIGN APPLICATIONS ***** <i>None AC</i>				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/29/2000				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		STATE OR COUNTRY CA	SHEETS DRAWING 4	TOTAL CLAIMS 23
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature Initials		INDEPENDENT CLAIMS 3		
ADDRESS Bradley T. Sako 3954 Loch Lomand Way Livermore, CA 94550				
TITLE Architecture for efficient implementation of serial data communication functions on a programmable logic device (PLD)				
FILING FEE RECEIVED 764	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	



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CONFIRMATION NO. 960

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APPLICANTS

Michael T. Moore, Santa Clara, CA;

James Lie, Milpitas, CA;

** CONTINUING DATA *****

None *PS*

** FOREIGN APPLICATIONS *****

None *PS*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Bradley T. Sako</i> Examiner's Signature	STATE OR COUNTRY CA	SHEETS DRAWING 4	TOTAL CLAIMS 23	INDEPENDEN CLAIMS 3
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ADDRESS

Bradley T. Sako
3954 Loch Lomand Way
Livermore, CA
94550

TITLE

Architecture for efficient implementation of serial data communication functions on a programmable logic device (PLD)

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